SHUBHAM SHARAN

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ADDRESS

F-2, 3rd Floor, Campa Cola Gali,
Lado Sarai, New Delhi,
Delhi - 110030

ACADEMIC DETAILS

Delhi Public School, R.k Puram
X | 2011
Marks – 8.4 CGPA

Delhi Public School, R.k Puram
XII | 2013
Marks - 82.16%,

Maharaja Surajmal Institute of Technology, GGSIPU
B.Tech | 2018
Marks – 78.37%

SKILLS

• SQL(MYSQL)
• C++
• Power BI
• MS Excel
• MS Office
• Hadoop
• Tanner EDA

EXTRA CURRICULAR ACTIVITIES

• Volunteer at WWF India.
• Volunteer at Greenpeace India.
• Volunteer at Mash Projects.
• Volunteer at World Skills India.

INTERESTS

• Nature Photography
• Skating

OBJECTIVE

Ambitious to kick start the career with globally recognized organization which will give me global exposure to enhance my skills and knowledge for mutual benefits of the organization.

EXPERIENCE

Content Associate at Zomato Media Pvt. Ltd (08/2018-01/2019)

TRAININGS

Big Data Analytics.
Learnt Hadoop, Apache Sqoop for extraction, Apache Pig and Apache Hive for transformation and to store, Apache Kafka, Yarn, PySpark, HBase.

Winter Training at ALTTC(BSNL)
Overview of Telecom Technologies.

Summer Training at Airport Authority of India
Overview of VHF, NAV-AIDS and ASMGCS

PROJECTS

Retail Chain Database
The project was comprised of 6 huge datasets with order details, customer details, orders items details and so on. I performed ETL-Extraction using Sqoop from RDBMS, transformation using Hive and Pig. Then I executed an SCD logic using HCatalog to synchronize the data with RDBMS data.

Airport Database
This project consists of 3 datasets, Airlines data, Routes data and Airport data. I performed ETL logic using Sqoop and Hive. Later completed it using Pig and Spark SQL and data frames

Loan Database
This project consists of 52 attributes like id, loan-amount, funded-amount, term, Interest rate, instalments and so-on. I performed ETL logic using Sqoop and Hive And also do some analysis using pig and Spark SQL.

Implementation of Very High-Performance D Flip Flop Using MTCMOS Technology
Designed a very high-performance D flip flop using multi threshold technology in Tanner EDA. The designed D flip flop has a very good operating speed and requires very less power in its operation in comparison to other D flip flops designed using other technologies like CMOS.

Implementation of UART Using Verilog
UART is universal asynchronous receiver transmitter device used for asynchronous serial communication designed this device using Verilog.